

## PUNYASHLOK AHILYADEVI HOLKAR SOLAPUR UNIVERSITY. SOLAPUR

## EXAM OF 2020 SEMESTER PATTERN EXAMINATION

FINAL PROGRAM OF M.SC. (ELECTRONIC SCIENCE)SEM – I TO IV MARCH 2020 EXAMINATION TO BE HELD IN OCT-2020

- 1. The Candidates at the above examination are requested to be present **FIFTEEN MINUTES** before the time starting of the first paper and **TEN MINUTES** before the time starting of each subsequent paper. Candidates are forbidden to take any book or paper into the Examination Hall.
- **2.** The Examination Programme is subject to change. If there is any change in the Examination Programme the University is not responsible for the loss or inconvenience caused to the candidates.
- **3.** The external candidates are requested to see their examination seat number as well as place of the examination on the notice board of the University/College at least two days before the date of examination.
- **4.** Cellular Phone, Pager, Scientific Calculator, Digital Diary or any means, which can be used for unfair practice, are not allowed in the Examination Hall.
- **5.** <u>Important Note:</u> Code Numbers given in the Bracket are Computer Code of respective Subjects, Students should mention these Code Numbers on Answer Books with Name of the Subject. Also write these Code Numbers on JSR (Junior Supervisor Report) and related documents.
- 6. The Time for the exam is one and haf hour (1 ½) only.
- 7. The written examination will be conducted online/offline mode which will be held at home/desegnated places.
- 8. All the student are here by informed that the time slot given in the time table shows 5 hrs for each day. But you will get 1 ½ hrs to solve the paper. If any student has two paper in one day that time he will be solving two papers within 5 hours.

M.Sc. (Electronic Science)Sem – I (w.e.f. June 2016) (CBCS Pattern-2016)						
Day &Date	Paper No.	Sr. No.	Subject TIME: 11:00 a.m. to 4:00 p.m. (Only 1 ½ hrs out of this slot)			
Monday 05/10/2020	HCT1.1	180	Semiconductor Devices (2178101)			
Tuesday 06/10/2020	HCT1.2	181	Network Analysis and Synthesis (2178102)			
Wednesday 07/10/2020	HCT1.3	182	Communication Systems (2178103)			
Thursday 08/10/2020	SCT1.1 SCT1.2	183 184	A)Microcontrollers and Interfacing (2178104) OR B)Electromagnetic and Antennas (2178105)			

M.Sc. (Electronic Science) Sem – II (w.e.f. Nov 2016) (CBCS Pattern-2016)						
Day &Date	Paper No.	Sr. No.	Subject TIME: 11:00 a.m. to 4:00 p.m. (Only 1 ½ hrs out of this slot)			
Monday 05/10/2020	HCT2.1	185	Control Systems (2178201)			
Tuesday 06/10/2020	HCT2.2	186	Digital Electronics and VHDL (2178202)			
Wednesday 07/10/2020	SCT2.1 SCT2.2	187 188	A) PIC Microcontroller (2178203) OR B) Foundation of Nanoelectronics (2178204)			
Saturday 17/10/2020	OET		[Choose from list]			

M.Sc. (Electronic Science)Sem – III (w.e.f. June 2017)							
(CBCS Pattern-2016)							
Day & Date	Paper No.	Sr. No.	Subject TIME: 11:00 a.m. to 4:00 p.m. (Only 1 ½ hrs out of this slot)				
Monday 05/10/2020	HCT3.1	189	Digital Signal Processing(2178301)				
Tuesday 06/10/2020	НСТЗ.2	190	Microwave Electronics and Applications (2178302)				
Wednesday 07/10/2020	SCT3.1	191	A) Data Communication and Networking (2178306)				
	SCT3.2	192	OR B) RTOS (2178307)				
Sunday 18/10/2020	OET		[Choose from list]				

M.Sc. (Electronics Science) Sem – IV(w.e.f. Nov 2017)						
(CBCS Pattern-2016)						
Day & Date	Paper No.	Sr. No.	Subject TIME: 11:00 a.m. to 4:00 p.m. (Only 1½ hrs out of this slot)			
Monday 12/10/2020	HCT4.1	193	Optic Fiber Communication (21784-01)			
Tuesday 13/10/2020	HCT4.2	194	Power Electronics (21784-02)			
Wednesday 14/10/2020	НСТ4.3	195	Advanced microcontroller (21784-03)			
Thursday 15/10/2020	SCT4.1	196	A] Satellite Communication (21784-06)  OR			
	SCT4.2	197	<b>B]</b> VLSI Design (21784-07)			

Please visit -http://su.digitaluniversity.ac Ref No.: PAHSUS/EXAM/TIME-TABLE/2020/778

Date : 19/09/2020

Sd/I/C Director
Board of Examinations and Evaluation